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Applicant: Lasserre

Art Unit: 2188

Serial No.: 09/932,381

Examiner: Reginald G. Bragdon

Filed: August 17, 2001

Docket: TI-31354

For: LOCAL MEMORY WITH INDICATOR BITS TO SUPPORT CONCURRENT DMA AND  
CPU ACCESS

**RESPONSE TO NOTICE OF DRAWING INCONSISTENCY WITH SPECIFICATION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)  
I hereby certify that the above correspondence is being deposited  
with the U.S. Postal Service as First Class Mail in an envelope  
addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria,  
VA 22313-1450 on September 9, 2005.

  
Robin E. Barnum

The following amendments and remarks are offered in response  
to the NOTICE OF DRAWING INCONSISTENCY WITH SPECIFICATION dated  
August 19, 2005. These amendments are respectfully submitted as a  
full and complete response to that Action.

Please amend the above referenced application as follows: